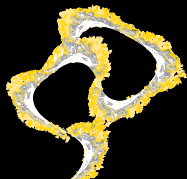
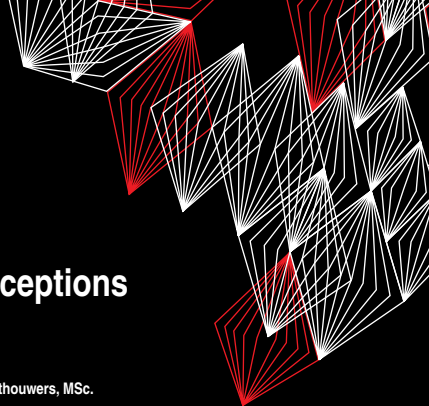


# Improving Support for Java Exceptions and Inheritance in VerCors

Bob Rubbens, BSc.

*Committee:* Prof. Dr. Marieke Huisman, Dr. Luís Ferreira Pires, Sophie Lathouwers, MSc.





# Overview

---

Verification of concurrent software

Deductive verification

Exceptions

Inheritance



Conclusion



# Overview

---

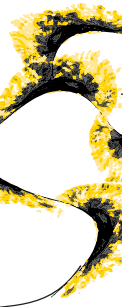
Verification of concurrent software

Deductive verification

Exceptions

Inheritance

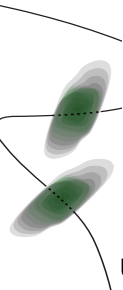
Conclusion

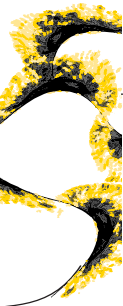


# Verification of concurrent software

---

Verification of concurrent software?

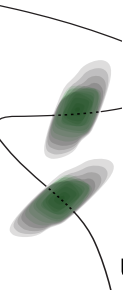




# Verification of concurrent software

---

Verification of concurrent software?



# “Verification”

---

- ▶ Verification:
  1. Verify that something works
  2. In relation to a specification
- ▶ Specification of a coffee machine



# Coffee machine specification

---



Action:  
When button is pushed



Result:  
Coffee must be produced

# Coffee machine specification

---



Action:  
When button is pushed



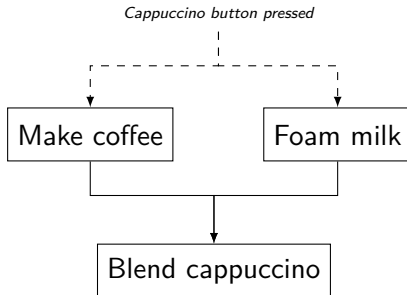
Result:  
Coffee must be produced

The challenge: verify an implementation against a specification  
automatically, statically



# “Concurrent”

- ▶ Concurrency means: interleaving of processes
- ▶ For cappuccino, need to foam milk & make coffee

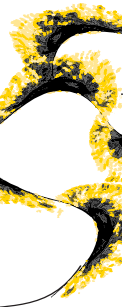




# “Software”

---

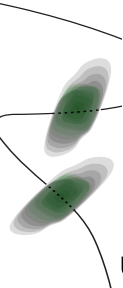
- ▶ Anything that regulates daily life through a computer or electronic device
  1. WhatsApp
  2. PowerPoint
  3. Internet bankieren

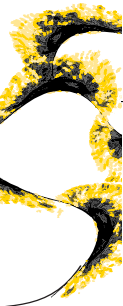


# Verification of concurrent software

---

Verification of concurrent software!  
Ensure it works      Interleaved      Software

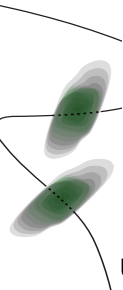


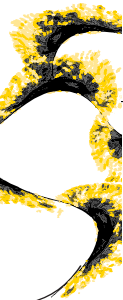


# Why verification?

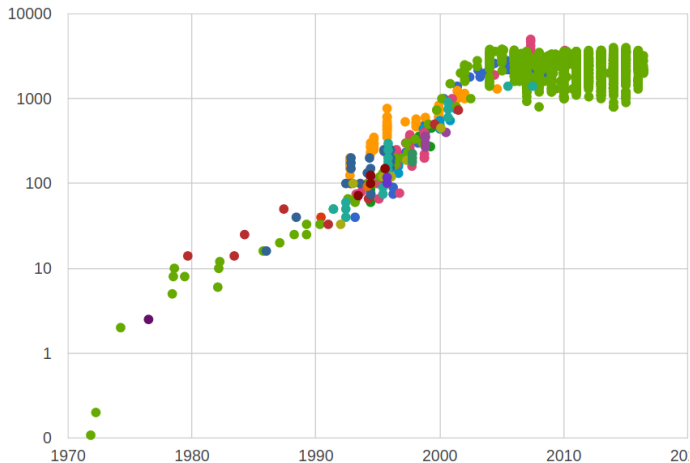
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- ▶ Design is trial and error
- ▶ Prevent bugs
- ▶ Automation





# Why concurrency?



(from Stanford CPUDB)



# Why software?

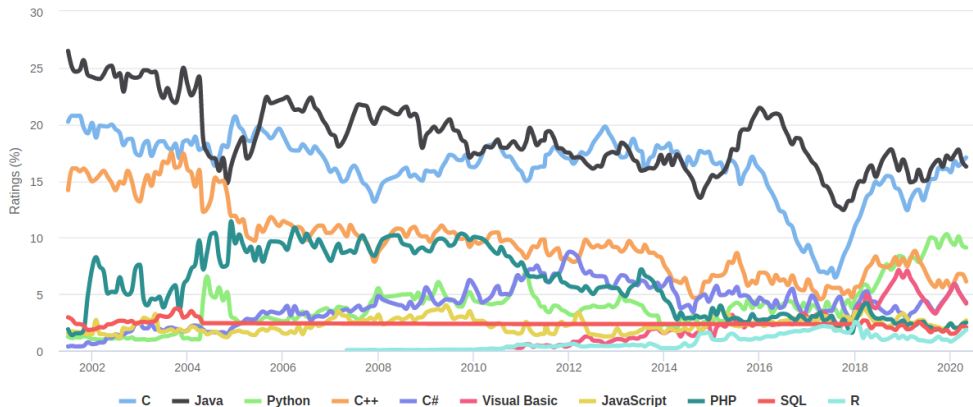
---

- ▶ Society has become dependant on software
- ▶ Therefore, we want to verify it

# Java

## TIOBE Programming Community Index

Source: [www.tiobe.com](http://www.tiobe.com)





# Verify Java?

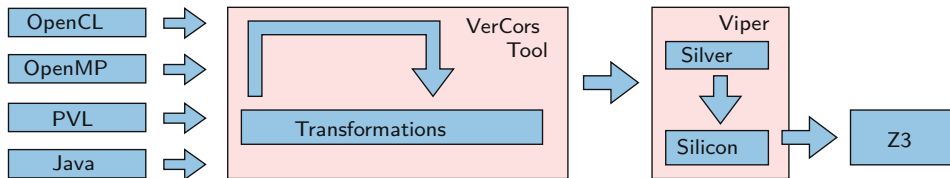
---

- ▶ Yes, with static verifiers!
- ▶ One example, topic of this presentation: VerCors
- ▶ Others:
  - ▶ Verifast
  - ▶ jStar
  - ▶ OpenJML
  - ▶ KeY
  - ▶ And more...



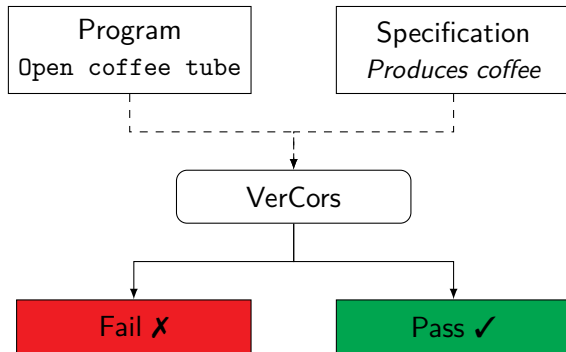
# VerCors

- ▶ Static deductive verifier for concurrent software
- ▶ Developed at the FMT group, University of Twente
- ▶ Java, C, OpenCL, PVL
- ▶ Data race freedom, memory safety, functional correctness



VerCors architecture

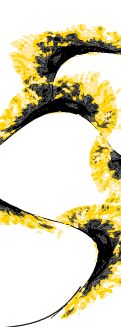
## VerCors usage





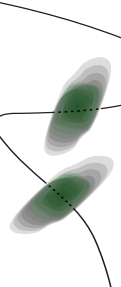
# So, why are there bugs?

---



# So, why are there bugs?

---

- ▶ Problem: support for commercial programming languages
    - ▶ Verifast, Nagini
  - ▶ “Advanced” language features
  - ▶ exceptions, inheritance, lambdas, streams
- 



# Overview

---

Verification of concurrent software

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Conclusion

# Deductive verification of Java

---

Using JML annotations in comments:

---

```
1  //@ requires a >= 0 && b >= 0;
2  //@ ensures a > b ? \result == a : \result == b;
3  int positive_max (int a, int b) {
4      //@ assert a >= 0;
5      if (a > b) {
6          return a;
7      } else {
8          return b;
9      }
10 }
11
12 positive_max(-1, 5);           // Fail
13 int x = positive_max(4, 10);  // Pass
14 //@ assert x == 10;           // Pass
```

---



## Separation logic

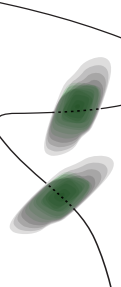
---

- ▶ Developed by John C. Reynolds, Peter O'Hearn, Samin Ishtiaq, and Hongseok Yang.
- ▶ Intended to describe ownership in programs with references.
- ▶ Turns out to also work surprisingly well for concurrent programs! (with some extensions)



# Permissions

---

- ▶  $\text{Perm}(x, f)$
  - ▶ Means:
    - ▶ Given heap location  $x$ ...
    - ▶  $f = 1 \implies$  Read/write  $x$
    - ▶  $0 < f < 1 \implies$  Read  $x$
  - ▶ Examples:
    - ▶  $\text{Perm}(x, 1/1)$
    - ▶  $\text{Perm}(\text{this.y}, 1/2)$
    - ▶  $\text{Perm}(\text{obj.field}, 3/6)$
- 





# Permissions

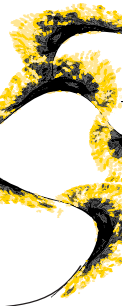
---

- ▶ A permission is a resource
- ▶ Finite: split/merge, but not duplicate
- ▶ Examples:

---

```
1  assert Perm(x, 1/1);  
2  assert Perm(x, 1/2) ** Perm(x, 1/2);  
3  assert Perm(x, 1/1);  
4  assert Perm(x, 1/1) ** Perm(x, 1/1); // Fails!
```

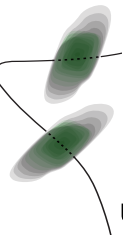
---



## VerCors backend: Viper

---

- ▶ Developed at ETH Zürich
- ▶ Verifies simple language with permissions
- ▶ “Silver”





# Java to Silver

---

## 1: Java

---

```
1  //@ ensures \result == 3;
2  void m() {
3      int x = 2;
4      return x + 1;
5  }
```

---

## 2: Silver

---

```
1  method m()
2      returns (res: Int)
3      ensures res == 3
4  {
5      var x: Int;
6      x := 2;
7      res := x + 1;
8  }
```

---



# Overview

---

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Conclusion

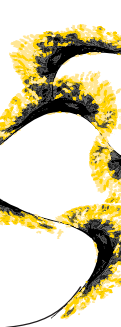


# Exceptions

---

```
1 void close() throws Exception {
2     if (f == null) {
3         throw new Exception("f is null");
4     } else {
5         f.close();
6     }
7 }
8
9 void doWork() {
10     try {
11         close();
12     } catch (Exception e) {
13         print("Something went wrong!");
14     }
15 }
```

---



# Exception specification


---

```
1  //@ signals (Exception e) f == null;
2  void close() throws Exception {
3      if (f == null) {
4          throw new Exception("f is null");
5      } else {
6          f.close();
7      }
8  }
```

---

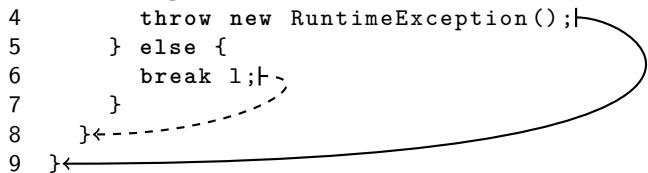
# Abrupt termination

```
1 void m() {  
2     l: while(p()) {  
3         if (p()) {  
4             throw new RuntimeException();  
5         } else {  
6             break l;  
7         }  
8     }  
9 }
```



# Abrupt termination

```
1 void m() {  
2     1: while(p()) {  
3         if (p()) {  
4             throw new RuntimeException();  
5         } else {  
6             break 1;  
7         }  
8     }  
9 }
```




No problem, right?



# Abrupt termination to goto


```
1 void close() {  
2     l: while(p()) {  
3         if (p()) {  
4             goto close_end;|  
5         } else {  
6             goto l_end;|---  
7         }  
8     } l_end:←---  
9 close_end: }←
```

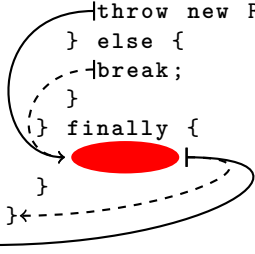


# Abrupt termination & finally


```
1 void close() {  
2     while(p()) {  
3         try {  
4             if (p()) {  
5                 throw new RuntimeException();  
6             } else {  
7                 break;  
8             }  
9         } finally {  
10            }  
11     }  
12 }  
13 }
```

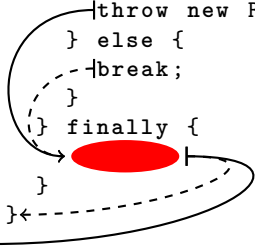
# Abrupt termination & finally

```
1 void close() {  
2     while(p()) {  
3         try {  
4             if (p()) {  
5                 throw new RuntimeException();  
6             } else {  
7                 break;  
8             }  
9         } finally {  
10              
11        }  
12    }  
13 }
```




# Abrupt termination & finally

```
1 void close() {  
2     while(p()) {  
3         try {  
4             if (p()) {  
5                 throw new RuntimeException();  
6             } else {  
7                 break;  
8             }  
9         } finally {  
10              
11        }  
12    }  
13 }
```



“finally encoding problem”



# Approaches to the finally encoding problem

---

## 1. Inlining

- ▶ Inflates AST
- ▶ Duplicates proof obligations


# Approaches to the finally encoding problem

1. Inlining
2. Auxiliary state

```
1  if (p()) {  
2    break;  
3  } else {  
4    return;  
5  }
```

⇒

```
1  if (p()) {  
2    mode = BREAK;  
3    goto finally;  
4  } else {  
5    mode = RETURN;  
6    goto finally;  
7  }
```



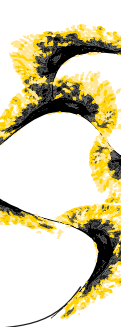
# Approaches to the finally encoding problem

---

1. Inlining

2. Auxiliary state

- ▶ Creates constants to keep track of in the presence of labeled break
- ▶ Leads to non-modular finally

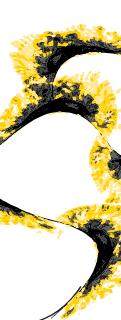


# Approaches to the finally encoding problem

---

1. Inlining
2. Auxiliary state
3. Via exceptions





# Encode finally via exceptions

---

Consider finally with *only* exceptions:

---

```
1  try {  
2    ...  
3  } catch (Exception e) {  
4    ...  
5  } finally {  
6    ...  
7    if (exception) {  
8      goto next_handler;  
9    } else {  
10     goto after;  
11   }  
12 }  
13 after:
```

---

# Encode finally via exceptions

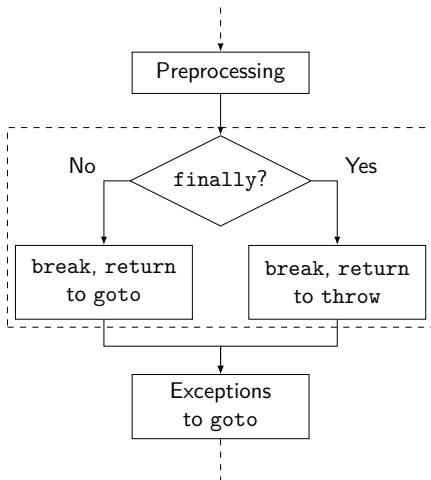
- ▶ This only works if there is only exceptional control flow
- ▶ That is possible:

```
1  1: while(p()) {  
2      ...  
3      break 1;  
4      ...  
5  }
```

⇒

```
1  try {  
2      while(p()) {  
3          ...  
4          throw new L();  
5          ...  
6      }  
7  } catch (L e) {};
```

# Implemented abrupt termination transformation





# Overview

---

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# Coffee machine inheritance



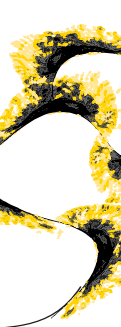
# Coffee machine inheritance



Parent, super



Child, sub



# Inheritance example

---

```
1  class Cell {
2      int val;
3      void set(int newVal) {
4          val = newVal;
5      }
6  }
7
8  class ReCell extends Cell {
9      int bak;
10     void set(int newVal) {
11         bak = super.get();
12         super.set(newVal);
13     }
14 }
```

---



## Example with plain contracts

---

```
1  //@ requires true;
2  //@ ensures val == newVal;
3  void Cell.set(int newVal) {
4      val = newVal;
5  }
6
7  //@ requires true;
8  //@ ensures bak == \old(val) && val == newVal;
9  void ReCell.set(int newVal) {
10     bak = super.get();
11     super.set(newVal);
12 }
```

---





## Behavioural subtyping, informally

---

Wherever a parent method is used, a child method should also be usable.



## Behavioural subtyping, informally

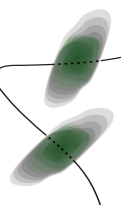
---

Wherever a parent method is used, a child method should also be usable.

In terms of contracts:

Definition (Method Subtyping)

Given a method *requires*  $P$ ; *ensures*  $Q$ ;  $f()$  and  $f'$  that overrides it,  $f'$  is a behavioral subtype of  $f$  if:



- ▶  $P \implies P'$

- ▶  $Q' \implies Q$



## Plain contracts subtype

---

```
1  //@ requires true;
2  //@ ensures val == newVal;
3  void Cell.set(int newVal);
4
5  //@ requires true;
6  //@ ensures bak == \old(val) && val == newVal;
7  void ReCell.set(int newVal);
```

---

`true ==> true`

`(bak == \old(val) && val == newVal)  
==> (val == newVal)`



## Example with separation logic contracts

---

```
1  //@ requires Perm(val, 1/1);
2  //@ ensures Perm(val, 1/1) ** val == newVal;
3  void Cell.set(int newVal);
4
5  //@ requires Perm(val, 1/1) ** Perm(bak, 1/1);
6  /*@ ensures Perm(val, 1/1) ** Perm(bak, 1/1)
7           ** bak == \old(val)
8           ** val == newVal; @*/
9  void ReCell.set(int newVal);
```

---

## Example with separation logic contracts

```
1  //@ requires Perm(val, 1/1);
2  //@ ensures Perm(val, 1/1) ** val == newVal;
3  void Cell.set(int newVal);
4
5  //@ requires Perm(val, 1/1) ** Perm(bak, 1/1);
6  /*@ ensures Perm(val, 1/1) ** Perm(bak, 1/1)
7           ** bak == \old(val)
8           ** val == newVal; @*/
9  void ReCell.set(int newVal);
```

pre-condition Cell ==> pre-condition ReCell

Perm(val, 1/1) ==> Perm(val, 1/1) \*\* Perm(bak, 1/1)

## Example with separation logic contracts

```
1  //@ requires Perm(val, 1/1);
2  //@ ensures Perm(val, 1/1) ** val == newVal;
3  void Cell.set(int newVal);
4
5  //@ requires Perm(val, 1/1) ** Perm(bak, 1/1);
6  /*@ ensures Perm(val, 1/1) ** Perm(bak, 1/1)
7           ** bak == \old(val)
8           ** val == newVal; @*/
9  void ReCell.set(int newVal);
```

pre-condition Cell ==> pre-condition ReCell

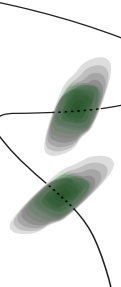
Perm(val, 1/1) ==> Perm(val, 1/1) \*\* Perm(bak, 1/1)

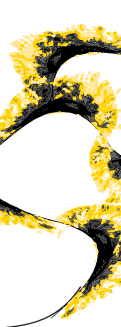
**X** Not subtype



## Solution: abstract predicate families

---

- ▶ Abbreviated: APF
  - ▶ Defines “name” shared between classes
  - ▶ Class can choose “contents”
  - ▶ Two forms:
    - ▶ “Generic”: `state()`
    - ▶ “Specific”: `state@Cell()`
  - ▶ “Generic” **\*\*** dynamic type  $\iff$  “specific”
  - ▶ “Specific”  $\iff$  “contents”
- 



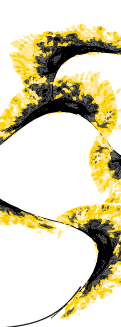
## APF: Cell

---

```
1  /*@ resource state(int x) = Perm(val, 1/1)
2      ** val == x; @*/
3
4  //@ requires state(oldVal);
5  //@ ensures state(newVal);
6  void Cell.set(int newVal) {
7      //@ unfold state(oldVal);
8      //@ unfold state@Cell(oldVal);
9      //@ assert Perm(val, 1/1);
10 }
11
12 //@ requires state(oldVal, oldBak);
13 //@ ensures state(newVal, oldVal);
14 void ReCell.set(int newVal);
```

---



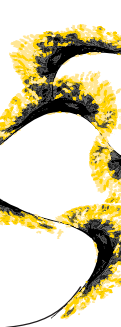


## APF: Behavioural subtype?

---

`state(oldVal) ==> state(oldVal, oldBak)`

`state(newVal, oldVal) ==> state(newVal)`



## APF: Behavioural subtype?

---

`state(oldVal) ==> state(oldVal, oldBak)`

`state(newVal, oldVal) ==> state(newVal)`

✓ APFs allow behavioural subtyping



# APF exchange problem

---

- ▶ “Generic” \*\* dynamic type  $\iff$  “specific”
- ▶ Dynamic type is not known: only subtype
  - ▶ super



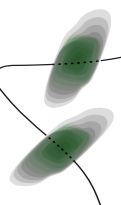
# APF exchange problem

---

```
1  //@ requires state(oldVal);
2  void Cell.set(int newVal) {
3      //@ assert this == Cell; // Maybe...?
4      //@ assert this == ReCell; // Maybe...?
5      //@ assert this instanceof Cell; // True
6      //@ unfold state(oldVal); // Not allowed
```

---

For example:



```
void ReCell.set(int x) {
    // Dynamic type != Cell
    super.set(x);
}
```

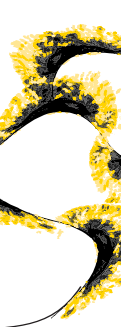


# Resolving the APF exchange problem

---

1. “Non-modular”
2. “Extension”
3. “Static/dynamic”

Suggested for VerCors: combine extension & static/dynamic



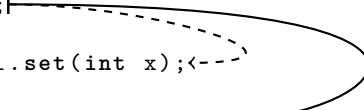
# Static/dynamic

---

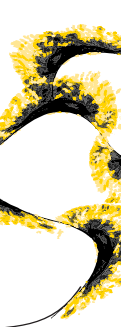
- ▶ Insight: dynamic dispatch  $\implies$  dynamic type
- ▶ “Generic” \*\* dynamic type  $\iff$  “specific”

---

```
1 Cell c = ...;  
2 c.set(3);  
3  
4 void Cell.set(int x);  
5  
6 void ReCell.set(int x);
```



---



# Static/dynamic example

---

```
1  //@ requires state(oldVal);  
2  void Cell.set(int newVal) {  
3      //@ assert state@Cell(oldVal);
```

---



## Static/dynamic trade-offs

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- ▶ Benefit: modular, allows modelling parameters
- ▶ Drawback: complicated, no side-calling

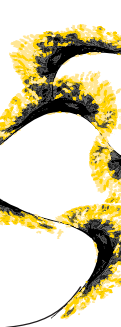




## Extension

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- ▶ Insight: APFs the parent APF
- ▶ extract statement



## extract example

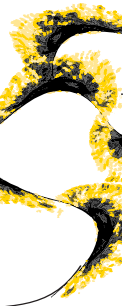
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- ▶ “Generic” `** instanceof`  $\iff$  “partial specific”
- ▶ “Partial specific” `** instanceof`  $\iff$  “generic”

---

```
1 Cell c = ...;  
2 //@ assert c.state(oldVal);  
3 //@ extract c.state@Cell(oldVal);  
4 //@ assert c.state@Cell(oldVal);
```

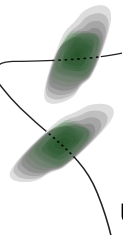
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## Extension trade-offs

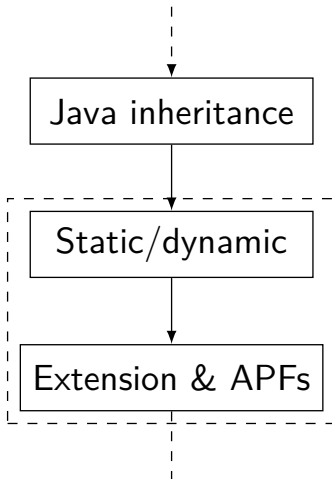
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- ▶ Benefits:
  - ▶ Straightforward to explain.
  - ▶ Integrates well with VerCors.
- ▶ Drawbacks:
  - ▶ Parent APF inclusion is mandatory.
  - ▶ extract is read-only.



## Suggested transformation

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# Overview

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Verification of concurrent software

Deductive verification

Exceptions

Inheritance

Conclusion



## Future work

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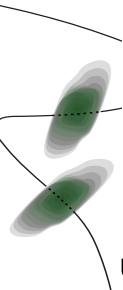
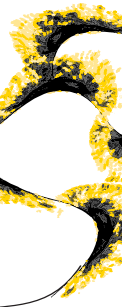
- ▶ Formal proof of correctness
- ▶ Further improving language support
- ▶ Standard library specification
- ▶ Improve theory of inheritance



## Conclusion

---

- ▶ Static verifiers do not support commercial languages enough
- ▶ Abrupt termination can be encoded in exceptions
- ▶ VerCors could support inheritance through combined approaches
- ▶ Concluding:
  - ▶ Full exception support is achievable
  - ▶ Basic inheritance is possible with trade-offs.



## Bonus slides





Name	Development	Viper	Concurrency	Exceptions	Inheritance
Nagini	Current	Yes	Full	Yes	Yes
Prusti	Current	Yes	Implicit	No	No
Soothsharp	Prototype	Yes	Implicit	No	No
Rust2Viper	Prototype	Yes	Implicit	No	No
Scala2Sil	Prototype	Yes	Implicit	No	No
Frama-C	Current	No	Full	No	No
Verifast	Current	No	Full	Up to finally	Yes
KeY	Current	No	No		Yes
OpenJML	Current	No	No		Yes
JaVerT	No	No	No	Yes	No
K	Current	No	Full	—	—
Spec#	No	No	No	Yes	Yes
jStar	No	No	Implicit	No	Yes
LOOP	No	No	No	Yes	Yes
Krakatoa	No	No	No	Yes	No
VCC	No	No	Full	—	—
Caper	Unclear	No	Implicit	—	—
Why3	Current	No	No	Yes	No

## continue to break

---

```
1  l: while(p()) {  
2      ...  
3      continue l;  
4      ...  
5  }
```

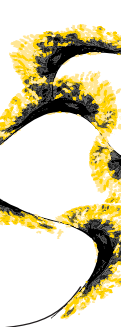
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⇒

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```
1  l: while(p()) {  
2      inner_l: {  
3          ...  
4          break inner_l;  
5          ...  
6      }  
7  }
```

---



## return to throw

---

---

```
1 void m() {  
2     ...  
3     return v;  
4     ...  
5 }
```

---

⇒

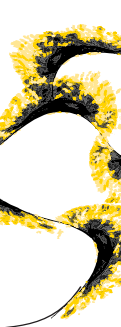
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```
1 void m() {  
2     try {  
3         ...  
4         throw new R_m(v);  
5         ...  
6     } catch (R_m e) {  
7         return e.value;  
8     }  
9 }
```

---



```
1  try {  
2      loopA: while (p) {  
3          while (q) {  
4              try {  
5                  if (r) {  
6                      break;  
7                  } else if (s) {  
8                      break loopA;  
9                  } else {  
10                     return;  
11                 }  
12             } finally {  
13                 /* Ambiguity */  
14             }  
15         }  
16     }  
17 }  
18  
19 } finally {  
20     ...  
21 }  
22  
23 }
```

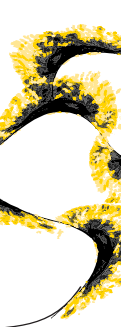


## Extension & locking

---

```
1 class Cell {
2     int val;
3     //@ resource lock_invariant() = Perm(val, 1\1);
4 }
5 void doWork(Cell c) {
6     synchronized (c) {
7         //@ assert c.lock_invariant();
8         //@ extract c.lock_invariant@Cell();
9         //@ unfold c.lock_invariant@Cell();
10        c.val = c.val + 2;
11        //@ fold c.lock_invariant@Cell();
12        //@ apply c.lock_invariant@Cell() -* c.lock_invariant();
13        //@ assert c.lock_invariant();
14    }
15 }
```

---



## extract read-only

---

```
1  //@ resource state(int x) = Perm(val, 1\1) ** val == x;
2
3  //@ requires state(oldVal);
4  void set(Cell c, int newVal) {
5      //@ extract c.state@Cell(oldVal);
6      //@ unfold c.state@Cell(oldVal);
7      c.val = newVal;
8      //@ fold c.state@Cell(newVal);
9      //@ assert c.state@Cell(oldVal) -* c.state(oldVal);
10     // Impossible:
11     //@ apply c.state@Cell(newVal) -* c.state(newVal);
12 }
```

---